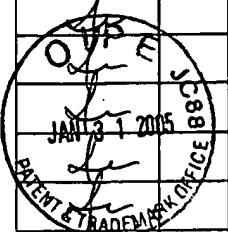
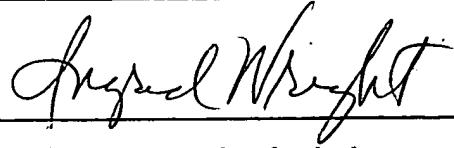


Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5681-74100  APPLICANT: Kim  FILING DATE: March 24, 2004	SERIAL NO. 10/807,743  GROUP: 2835
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## U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
		6,723,915	4/20/04	Radu, et al.			
		5,586,011	12/17/96	Alexander			
		6,294,407	9/25/01	Jacobs			
		6,219,254	4/17/01	Akerling, et al.			
		6,239,359	5/29/01	Lilienthal, II, et al.			
		6,088,231	7/11/00	Fajardo			
		6,252,313	6/26/01	Zhang, et al.			
		6,362,977	3/26/02	Tucker, et al.			
		6,278,617	8/21/01	Yang, et al.			
		6,219,239	4/17/01	Mellberg, et al.			
		6,288,330	9/11/01	Chen			
		6,259,609	7/10/01	Kurz			
		6,198,630	3/6/01	Cromwell			
		5,357,404	10/18/94	Bright, et al.			
		6,327,074	11/27/01	Lunden			
		6,269,863	8/7/01	Wyler			
		6,140,577	10/31/00	Rapaich, et al.			
		6,137,051	10/24/00	Bundza			
		5,825,634	10/20/98	Moorehead, Jr.			
		5,748,455	5/5/98	Phillips, et al.			
		5,566,052	10/15/96	Hughes			
		6,683,796	1/27/04	Radu, et al.			
		6,057,600	5/2/00	Kitazaw, et al.			
		6,400,164	6/4/02	Sampath			
		6,063,999	5/16/00	Kelly			
		5,166,772	11/24/92	Soldner, et al.			

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## U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
		5,561,265	10/1/96	Livshits, et al.			
		5,639,989	6/17/97	Higgins, III			
		6,573,590	6/3/03	Radu, et al.			
		6,483,406	11/19/02	Sawa, et al.			
		6,703,704	3/9/04	Alcoe, et al.			
		6,597,575	7/22/03	Matayabes, et al.			
		6,320,257	11/20/01	Jayaraj, et al.			
		6,297,551	10/2/01	Dudderar, et al.			
		5,294,826	3/15/94	Marcantonio, et al.			
		6,525,407	2/25/03	Drewery			
		6,339,253	1/15/02	Corisis			

## FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	A1	03/041466	5/15/03	WO			

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	A2	Radu, et al., "Investigation of Internal Partitioning in Metallic Enclososures for EMI Control," Sun Microsystems, Inc, et al., 6/97, pp. 171-176.
	A3	Article: "Identifying an EMI Source and Coupling Path in a Computer System with Sub-Module Testing"; Radu, et al.; Electromagnetic Compatibility Laboratory, Dept. of Electrical Engineering, University of Missouri-Rolla, Rolla, MO and Electromagnetic Compatibility Group, Sun Microsystems, Mountain View, CA; pgs. 165-170.
	A4	Article: "Intel® Pentium® 4 Processor in the 423-Pin Package EMI Guideline"; October 2000; ©Intel Corporation.
	A5	Article: "Mechanical Enabling Efforts"; Fall 2000; Intel Developer Forum; ©Intel Corporation.

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**EXAMINER:**

Frederick Wright

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